

## Dhirajlal Gandhi College of Technology



# Department of Electronics and Communication Engineering

News Letter (June –Dec)2019

# Dhirajlal Gandhi College of Technology







Chairman: Shri. Dhirajlal A Gandhi

Secretary : Smt.ArchanaManojkumar

Principal: Dr.S.Saravanan

# Department of Electronics and Communication Engineering



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### News Letter (June-Dec 2019)

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#### **Department of Electronics & Communication Engineering**

The objective of the ECE department is to foster the students to cultivate academic integrity with professional and social responsibility to meet the exponentially growing requirement of practical design engineers in the country and abroad. The department has highly qualified, experienced and dedicated faculty members who have a strong commitment towards providing quality engineering education with high emphasis of electronics design aspects and to bring out quality research experience to the students in order to help them excel in current competitive scenario. The mission of the department is to impart quality education to the students, an education that includes the skills of innovation, problem solving, consensus-building and those of entrepreneurship, so that they meet the challenging requirements of the global market with competence in ethical and moral reasoning.

#### Vision

To be globally recognized for providing value based engineering education and research in the field of Electronics and Communication Engineering.

#### Mission

- To impart quality education to students that includes skills of innovation, problem solving, teamwork and techno entrepreneurship.
- To craft professionals to meet the challenging requirements of global market and provide solutions to societal problems with ethical and moral reasoning.
- To establish Centre of Excellence in emerging areas in the field of Electronics and Communication Engineering.
- To undertake Collaborative work with industry to provide solutions for technical challenges.

#### Knowledge transferred is Knowledge gained





# Department of Electronics and Communication

-Place for freedom of creativity and innovation

## RF and Communication Systems Lab Inauguration.

## RF and Communication systems inauguration was conducted on **01.11.2019**



#### **Project Expo:**

As a part of RF and Communication Systems inauguration, project and poster contest was conducted on **01.11.2019**.



#### **BEC Training.**

**BEC Training** was conducted for Third Year Students A & B section on **25.07.2019.** 



#### **Tessolve Campus Drive:**

**Tessolve Campus Drive** was conducted on **17.07.2019**.



<u>Orientation ProgramforNew Faculty</u> members.

Orientation ProgramforNew Faculty members was conducted on 13.07.2019



#### **Value Added Program:**

Lab VIEW Core I Program was conducted in NI Lab on 11.07.2019 to 13.07.2019.



#### **Cyber Safety for Women Program**

**Cyber Safety for Women Program** from ECE Department **Second Year Students** was conducted on 12.07.2019.



#### **Workshop:**

Students **Workshop on VLSI Design using Verilog HDL( Webinar)** was conducted on **01.07.2019**.



#### **Students Interaction:**

Second Year Students Interaction for Result, Academic, Training& Placement was organized on 29.06.2019.



#### **LIVE WIRE:**

Third Year Students "LIVE WIRE" Program, Introduction to Recent Trends in Engineering, was conducted on 29.06.2019.



## Interaction for Result, Academic, Training & Placement:

Third Year Students Interaction for Result, Academic, Training & Placement.



#### **TSTE Course:**

Final Year Students "TSTE Test Engineering Course" Program was conducted on 01 June to 15 Dec, 2019.



#### **FACULTY ACHIEVEMENTS**

#### Faculty Achievements list:

Sl. No	Name of the staff	Title of the programme	Name of the organizer	Venue	Period From - To
1.	V.Arun	Anna University Sponsored FDTP On Ec8451 Electromagnetic Fields	Department of ECE	Government College of Engineering,Dha rmapuri	18-11-2019 to 23-11- 2019
		Basic Electric Circuits	NPTEL	Online Course	12 Weeks
2	R.ARUL	Comparative Analysis of 16 BIT SRAM using Various SRAM Cells in 45 nm CMOS Technology.	JOICS Journal	-	-
3	D.Rajeswari	Physics of Materials	NPTEL	Online Course	12 Weeks

